



B 2114

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/822,052
	Filing Date	March 30, 2001
	First Named Inventor	Sanjay Pillay
	Art Unit	2114
	Examiner Name	Gabriel Chu
Total Number of Pages in This Submission	Attorney Docket Number	1138-EP

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	THOMPSON & KNIGHT LLP	
Signature	James J. Murphy	
Date	December 10, 2004	

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PATENT
U.S. 09/822,052

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sanjay Pillay

Serial No.: 09/822,052

Filed: March 30, 2001

Title: CIRCUITS AND METHODS FOR DEBUGGING AN EMBEDDED
PROCESSOR AND SYSTEMS USING THE SAME

Confirm No.: 8450

Art Unit: 2114

Examiner: Gabriel Chu

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sirs:

Amendment Pursuant to 37 CFR §312

Pursuant to the Examiner's telephone request, Applicant has reviewed the present allowed application and has corrected discrepancies with respects to the tables and the table numbering. Due to the length of the application, Applicant provides herewith: (1) a replacement Detailed Description, including the tables, with the numbering discrepancies corrected; and (2) a marked – up copy of the Detailed Description indicating to the Examiner the changes which were made. No new matter has been added.

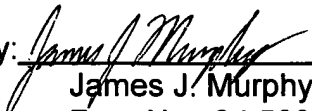
With the addition of no new claims, no additional filing fees are due. However, the Commissioner is hereby authorized to charge any fees or credit any overpayment to Deposit Account Number 20-0821 of Thompson & Knight LLP.

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PATENT
U.S. 09/822,052

If the Examiner has any questions or comments concerning this paper or the present application in general, the Examiner is invited to call the undersigned at (214) 969 - 1749.

Respectfully submitted,
Thompson & Knight LLP
Attorneys for Applicant

By: 
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Date: December 10, 2004